

Attorney Docket Number ___EFIM0252

#12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Stephan Voges
Serial No. :	09/521,280
Filed :	07-Mar-2000
For :	TCL_PLI, A FRAMEWORK FOR REUSABLE, RUN TIME CONFIGURABLE TEST BENCHES 2122 Eric B. Kiss FORMATION DISCLOSURE STATEMENT RECTION
Group Art Unit :	2122
Examiner :	Eric B. Kiss
INI Hon. Assistant Comm Washington, DC 2023	nissioner for Patents
Sir:	
Applicant submits thi	s Information Disclosure Statement:
application; wit international ap or before the ma	1.97(b) (within three months of the filing date of a national thin three months of the date of entry of the national stage in an oplication; before the mailing of a first Office action on the merits; ailing of a first Office action after the filing of a request for nination, whichever occurs last); or
_	1.97(c) (after the CFR 1.97(b) time period, but before the mailing a final action, a notice of allowance, or an action that otherwise
•	ion in the application) together with:
the staten	nent specified in 37 CFR §1.97(e); or
the fee se	et forth in 37 CFR §1.17(p); or
the issue fee) to	1.97(d) (after the CFR 1.97(c) time period, but before payment of ogether with: nent specified in 37 CFR §1.97(e); and et forth in 37 CFR §1.17(p).
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Applicant submits herewith PTO Form 1449 together with copies of patents, publications, applications, or other information listed therein. Applicant submits herewith a concise explanation of any patent, publication or other information listed in PTO Form 1449 that is not in the English language.

All of the references have been previously cited in parent application Serial

No. ______. Accordingly, Applicant has not provided copies of these references as they are readily found in the file of the parent application.

Applicant respectfully requests that the information disclosed herein be made of record in this application.

Respectfully submitted,

James Trosino

Registration No. 39,862 Attorney for Applicant

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PTO/SB/08A (10-01)

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2 of Sheet 1

Complete if Known		
Application Number	09 / 521,280	
Filing Date	07-Mar-2000	
First Named Inventor	Stephan Voges	
Art Unit	2122	
Examiner Name	Eric B. Kiss	
Attorney Docket Number	EFIM0252	4

			U.S. PAT	ENT DOCUMENTS	<u> </u>
Examiner Initials		Document Number Number - Kind Code ² (if known	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	FOREIGN PATENT DOCUMENTS					
Examiner Initials	Cite No. ¹	Foreign Patent Document Country Code 3 - Number 4 - Kind Code 5 (d known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Te
		WO 98/55879	12-10-1998	Hollander		
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control number Complete if Known Substitute for form 1449B/PTO 09 / 521,280 Application Number INFORMATION DISCLOSURE 07-Mar-2000 Filing Date Stephan Voges STATEMENT BY APPLICANT First Named Inventor Group Art Unit 2122 Eric B. Kiss **Examiner Name** (use as many sheets as necessary) EFIM0252 Attorney Docket Number of 2

Under the Paperwork

Signature

Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Amold, J. M., "An Architecture Simulator For National Semiconductor's Adaptive Processing Architecture (NAPA)," FPGAS For Custom Computing Machines, IEEE Comput. Soc., pp 271-72, April 15, 1998.	
		Parson D. et al., "A Framework For Simulating Heterogeneous Virtual Processors," Simulation Symposium, IEEE Comput. Soc., pp. 58-67, April 11, 1999.	
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